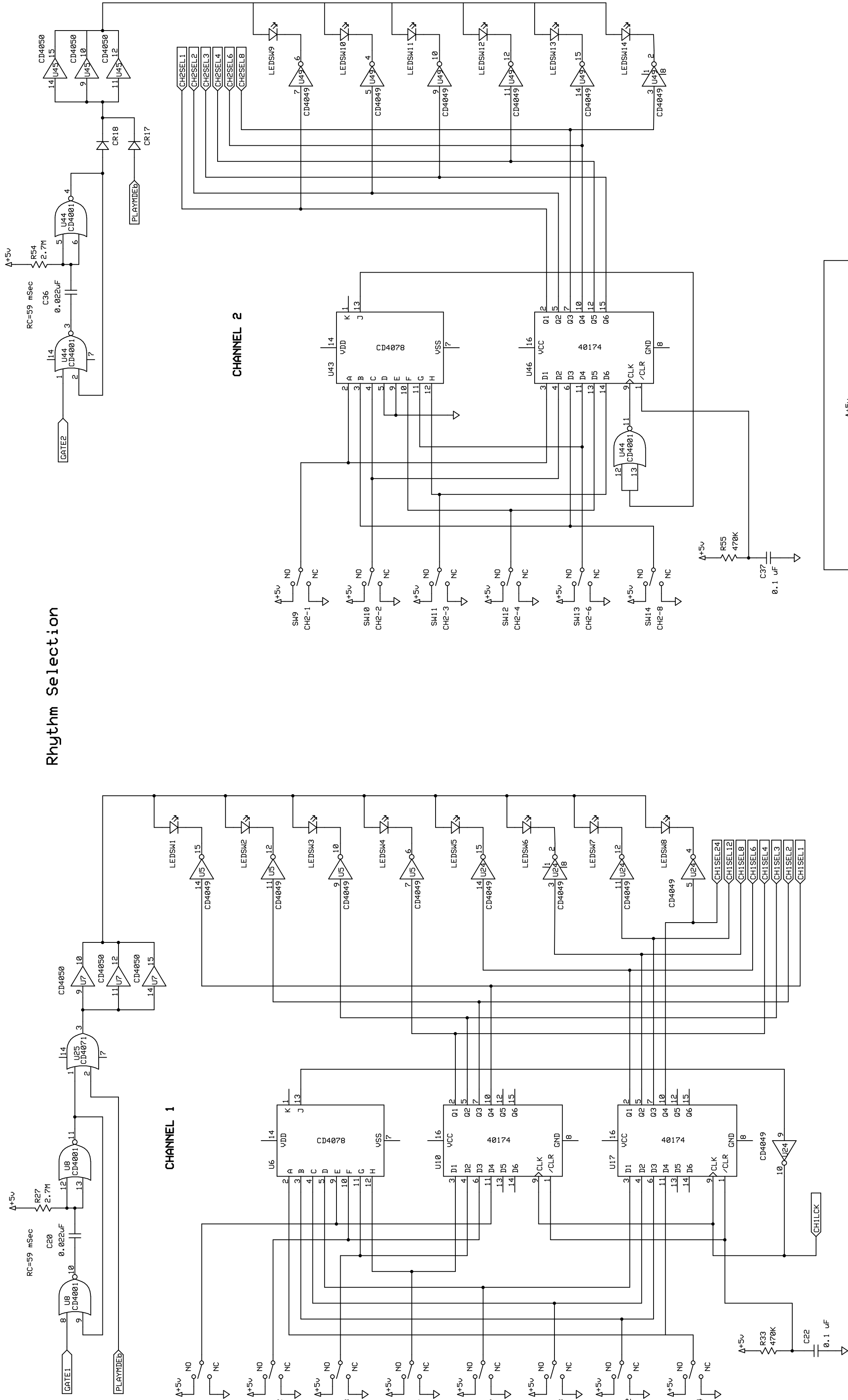
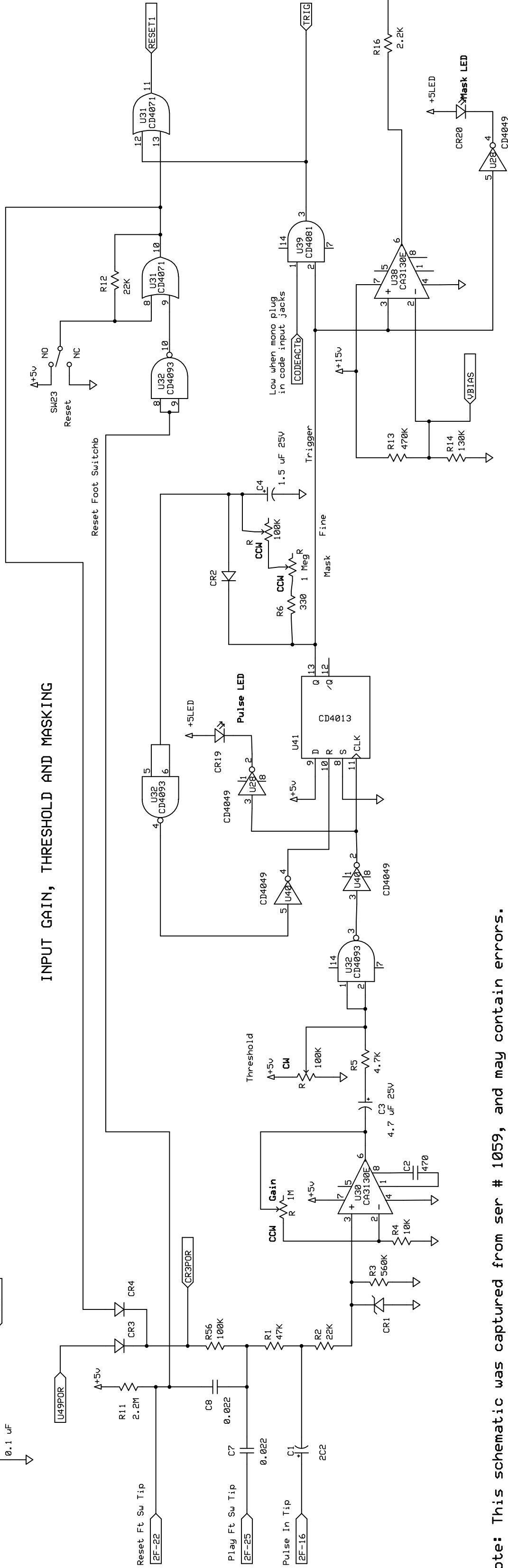


## Rhythm Selection



## INPUT GAIN, THRESHOLD AND MASKING



Note: This schematic was captured from ser # 1059, and may contain errors.