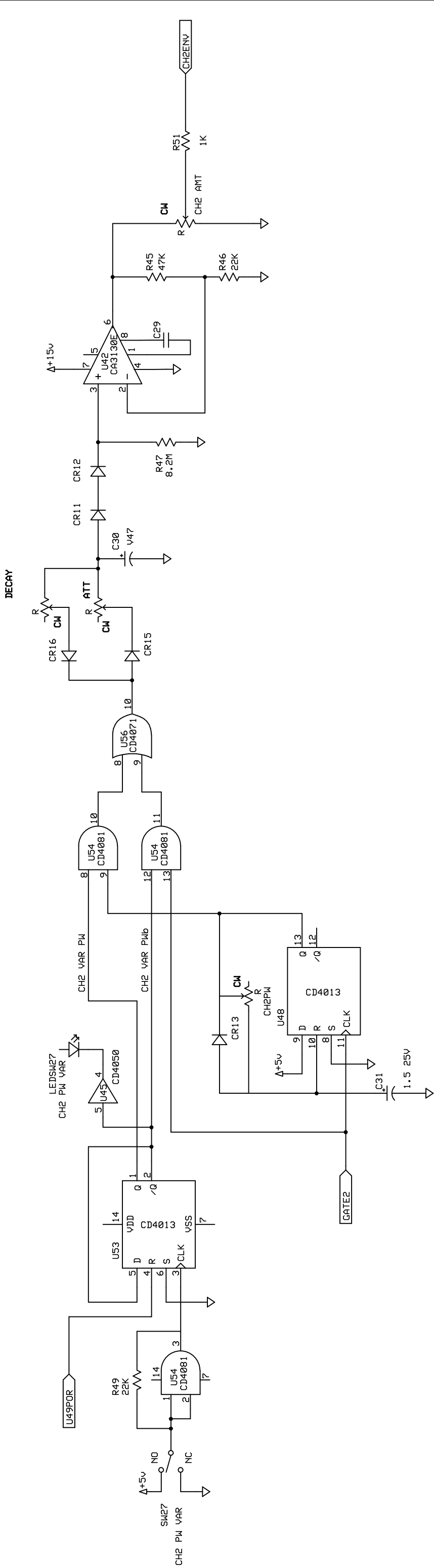
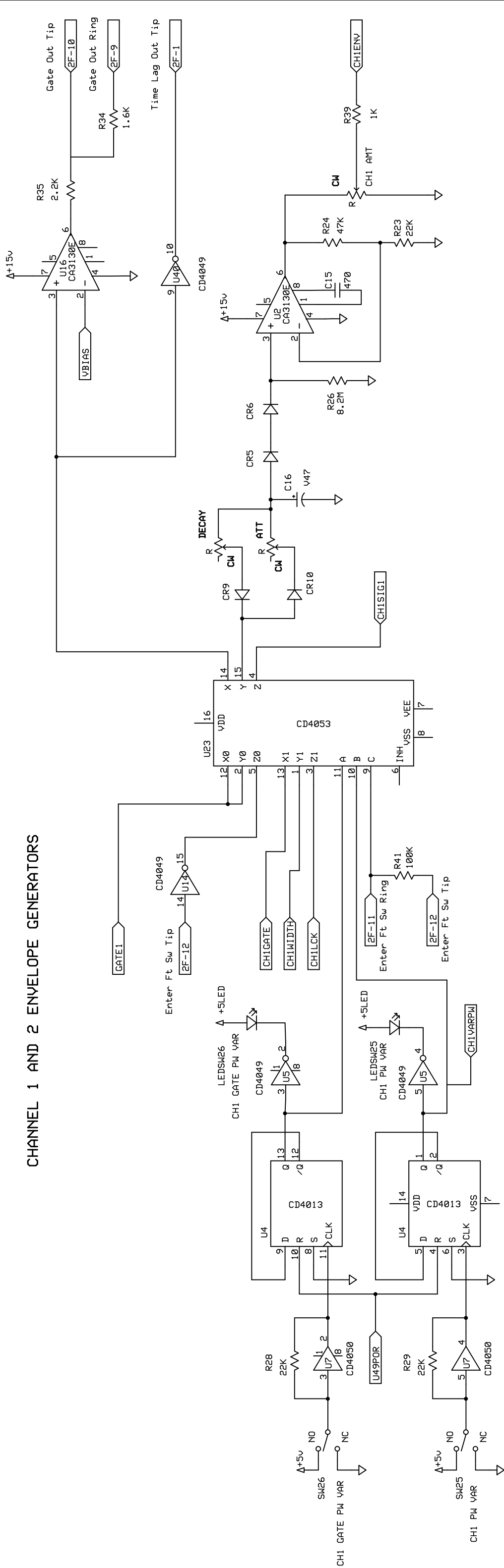
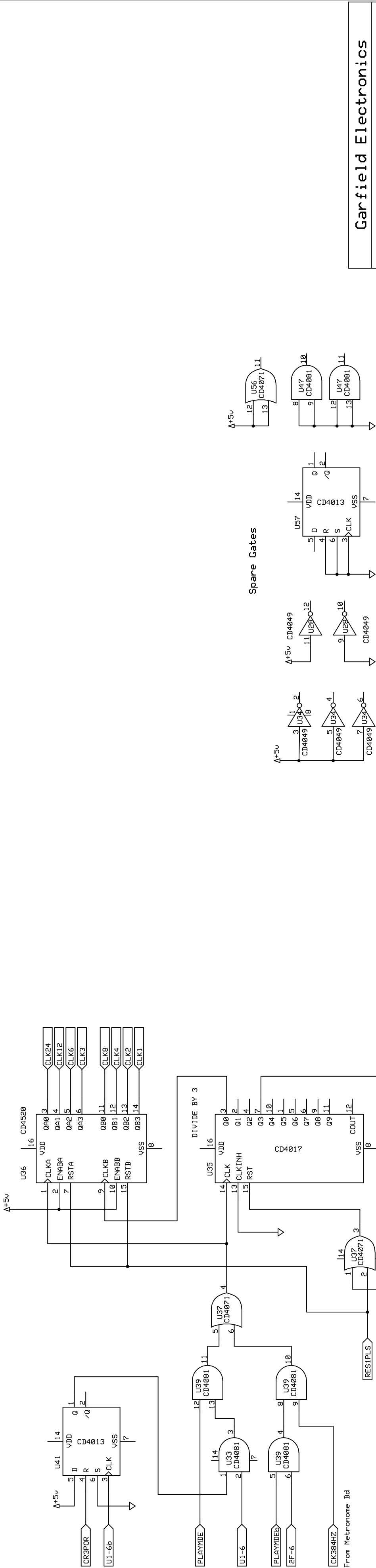


CHANNEL 1 AND 2 ENVELOPE GENERATORS



CLOCK GENERATION



Note: This schematic was captured from ser # 1059, and may contain errors.