1B 6502NMOS
1C 2516 2000H
1E 2516 2800H
1G 2516 3000H
1I 2516 3800H
2H 2114L
2I 2114L
2L D444C
2M D444C
2N D444C
2O D444C
WATCHDOG/RESET
74LS04
MAIN OSCILLATOR
Low to enable RAMs
EPROM chip Select de-glitch?
CONNA
CONNB
May contain errors.
Use at your own risk.
Schematic captured from Rev 1 CPU board, by R Grieb October 2021

Elka		
Synthex Rev 1 CPU Bd		
M M	Rev 1.0 10/05/2021	Page 1 of 2