

DAC full scale must be set first:

Remove the DAC chip and the CD4051B (U27 and U28)

With meter black lead connected to analog ground pt,
connect meter red lead to P5 5.12V eyelets.

Adjust trimmer potentiometer to get exactly 5.12VDC.

Install U27 and U28 and confirm that value is still correct.

Set parameters 63 and 64 to 0 to disable LFO modulation of CV's

Select PW for DCO1 (not PWM)

Set EG Mode to 1

Test Point	Conditions	Expected DC voltage
U31-7	PW = 32	0.503V
U31-7	PW = 8	0.894V
U29-7	Cutoff = 26	2.51
U29-7	Cutoff = 76	-2.54
U33-7	No modulation	0.025V
U32-7	Resonance = 23	8.0V
U32-7	Resonance = 90	-0.035V
U33-1	Attack = 10	-1.00V
U33-1	Attack = 80	-7.99V
U32-1	Decay = 10	-1.00V
U32-1	Decay = 80	-7.99V
U31-14	Sustain = 20	1.01V
U31-14	Sustain = 80	4.00V
U31-8	Release = 11	1.06V
U31-8	Release = 90	8.98V

All voltages +/- 0.04V due to op amp offsets and
resistor tolerances.